This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-9 (canceled)

Claim 10 (currently amended): A vidéo data recording and reproducing system for editing a source video data, said system comprising:

an analog video interface;

a digital video interface;

a digital video tape recording means for digitally recording a-source video data onto a tape medium at a first data rate during a source video data recording period and for reproducing recorded video data from said tape medium at said first data rate and at a second data rate which is faster than said first data rate to generate reproduced video data;

a disc recording means for recording said reproduced recorded video data onto a disc medium at said second data rate so that said source-reproduced recorded video data may be copied from said tape medium to said disc medium during a transfer period which is shorter than said recording period of said source video data;

a signal processing means for converting analog video data to digital video data and digital video data to analog video data;

a digital data transfer circuit for transferring digital video data between <u>said disc</u> recording means, said digital video tape recording means <u>interface</u> and said <u>disc recording means</u> <u>signal processor</u> at said first and second data rates, said digital data transfer circuit including buffer control means for controlling recording and reproduction of said disc recording means according to remaining capacity of at least one buffering means for buffering the digital video data;

said signal processor further adapted to switch signals between said analog video interface, said digital video tape recording means, and said digital data transfer circuit such that analog video data received over said analog video interface may be converted to digital video and stored on said digital video tape recording means as said source video data or a digital video signal received over said digital video interface may be transferred to said signal processor by

said digital data transfer circuit and stored on said digital video tape recording means as said source video data;

an editing means for controlling a reproducing operation of said disc recording means to generate edited video data which include a plurality of edit portions designated by an editing operation from said video data recorded on said disc medium,

wherein said editing means controls said reproducing operation of said disc recording means and said recording operation of said digital video tape recording means so that said edited video data may be reproduced from said disc medium and recorded on said tape medium at said second data rate.

Claim 11 (canceled)

Claim 12 (currently amended): The video data recording and reproducing system according to claim—11_10, wherein said first data rate is a real time video data rate.

Claim 13 (currently amended): The video data recording and reproducing system according to claim—11—10, wherein said video tape recording means has a first operation mode for recording said video data with said first data rate and for reproducing said video data with said first data rate and a second operation mode for recording said video data with said second data rate and for reproducing said video data with said second data rate;

wherein said disc recording means has a first operation mode for recording said video data with said first data rate and for reproducing said video data with said first data rate and a second operation mode for recording said video data with said second data rate and for reproducing said video data with said second data rate;

wherein said editing means controls said operation mode of said video tape recording means and said operation mode of said disc recording means.

Claim 14 (canceled)

Claim 15 (canceled)

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Claim 16 (currently amended): A video data recording and reproducing system for editing a source video data, said system comprising:

an analog video interface;

a digital video interface;

a digital video tape recorder configured to digitally record a source video data onto a tape medium at a first data rate during a source video data recording period, and configured to reproduce recorded video data from said tape medium at said first data rate and at a second data rate which is faster than said first transfer data rate to generate reproduced video data;

a disc recorder configured to record said reproduced recorded video data onto a disc medium at said second data rate so that said source-reproduced recorded video data may be copied from said tape medium to said disc medium during a transfer period which is shorter than said recording period of said source video data;

a signal processor adapted to convert analog video data and digital video data to analog video data;

a digital data transfer circuit for transferring digital video data between each of said digital video tape recording means and said disc recording means said digital video interface and said signal processor at said first and second data rates, said digital data transfer circuit including buffer control means for controlling recording and reproduction of said disc recording means according to remaining capacity of at least one buffering means for buffering the digital video data;

said signal processor further adapted to switch signals between said analog video interface, said digital video tape recording means, and said digital data transfer circuit such that analog video data received over said analog video interface may be converted to digital video and stored on said digital video tape recording means as said source video data or a digital video signal received over said digital video interface may be transferred to said signal processor by said digital data transfer circuit and stored on said digital video tape recording means as said source video data;

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an editing designation circuit configured to control a reproducing operation of said disc recorder to generate edited video data which include a plurality of edit portions designated by an editing operation from said video data recorded on said disc medium; and

a control circuit, coupled to said video tape recorder, said control circuit configured to control a recording operation of said video tape recorder to record said edited video data.

Claim 17 (currently amended): A method of editing video data, said method comprising the steps of:

providing a tape medium and a disc medium;

recording a source video data onto said tape medium with a first data rate during a recording period;

reproducing recorded source video data from said tape medium with a second data rate which is higher than said first transfer rate to generate reproduced video data;

recording said reproduced video data onto said disc medium with said second data rate so that said source video data is copied from said tape medium to said disc medium during a transfer period which is shorter than said recording period of said source video data;

editing said source video data on said disc medium by designating a plurality of edit portions; and

generating an edited video data including said plurality of edit portions at one of said first data rate and said second data rate; and

one of recording said edited video data on said tape medium and providing said edited video data to an analog or digital video interface video.

Claim 18 (canceled)

Claim 19 (canceled)

Claim 20 (canceled)

Claim 21 (canceled

Appl. No. 08/923,369
Reply to Office Action of April 2, 2003

Claim 22 (canceled)

Claim 23 (canceled)

Claim 24 (canceled)

Claim 25 (previously presented): The video data recording and reproducing system according to claim 16,

wherein said video tape recorder has a first operation mode for recording said video data with said first data rate and for reproducing said video data with said first data rate and a second operation mode for recording said video data with said second data rate and for reproducing said video data with said second data rate;

wherein said disc recorder has a first operation mode for recording said video data with said first data rate and for reproducing said video data with said first data rate and a second operation mode for recording said video data with said second data rate and for reproducing said video data with said second data rate;

wherein said editing designation circuit is further configured to control said operation mode of said video tape recording means and said operation mode of said disc recording means.

Claim 26 (canceled)

Claim 27 (canceled)

Claim 28 (previously presented): The video data recording and reproducing system according to claim—11 10, wherein said data transfer circuit includes:

an input buffering means, coupled to said video tape recording means and said disc recording means, for buffering said reproduced video data; and

an output buffering means, coupled to said video tape recording means and said disc recording means, for buffering said edited video data; wherein said buffer control means is Flood

coupled to said input buffering means, said output buffering means, and said disc recording means, for controlling recording and reproduction of said disc recording means according to respective remaining capacities of said input and output buffering means, and starts reproduction of said disc recording means where the remaining recording capacity of said input buffering means or said output buffering means is less than a predetermined lower limit setting, and stops reproduction of said disc recording means where said remaining recording capacity of said input buffering means or said output buffering means is at least a predetermined upper limit setting.

Claim 29 (previously presented): The video data recording and reproducing system according to claim 10, wherein said data transfer means includes:

an input buffering means, coupled to said video tape recording means and said disc recording means, for buffering said reproduced video data; and

an output buffering means, coupled to said video tape recording means and said disc recording means, for buffering said edited video data;

wherein said buffer control means is coupled to said input buffering means, said output buffering means, and said disc recording means, for controlling recording and reproduction of said disc recording means according to respective remaining capacities of said input and output buffering means, and starts reproduction of said disc recording means where the remaining recording capacity of said input buffering means or said output buffering means is less than a predetermined lower limit setting and stops the reproduction of said disc recording means where said remaining recording capacity of said input buffering means or said output buffering means is at least a predetermined upper limit setting.

Claim 30 (previously presented): The video data recording and reproducing system according to claim 16, wherein said data transfer circuit includes:

a write buffer circuit, coupled to said video tape recorder and said disc recorder, said write buffer circuit configured to buffer said reproduced video data; and

a read buffer circuit, coupled to said video tape recorder and said disc recorder, said read buffer circuit configured to buffer said edited video data; FIJ

wherein said buffer control circuit is coupled to said input buffer circuit, said output buffer circuit, and said disc recorder, and configured to control recording and reproduction of said disc recording means according to respective remaining capacities of said input and output buffer circuits, and starts reproduction of said disc recording means where the remaining recording capacity of said input buffering means or said output buffering means is less than a predetermined lower limit setting and stops the reproduction of said disc recording means where said remaining recording capacity of said input buffering means or said output buffering means is at least a predetermined upper limit setting.